

nsmitted herewith for filing is the Patent Application of:

iventor: CHI-CHUN CHEN, TZU-LIANG LEE, SHIH-CHANG CHEN

METHOD OF FORMING DUAL GATE INSULATOR LAYERS FOR CMOS APPLICATIONS

Enclosed are:							
X	2 sheets of drawing(s) - formal.						
x	An assignment of the invention to Taiwan Semicondutor Manufacturing Co.						
	An associate p	are: 2 sheets of drawing(s) - formal. An assignment of the invention to Taiwan Semicondutor Manufacturing Co. An associate power of attorney Applicant claims small entity status					
	Request & Certification under 35 USC 122(b)(2)(b)(i)						
The filir	ng fee has been	calculated as shown be	low:				
		(Col. 1)	(Col. 2)	OTHER THAN	A SMALL ENTITY		
FOR:		NO. FILED	NO. EXTRA	RATE	FEE		
BASIC	C FEE				\$ 750.		
	L CLAIMS	33 -20=	13	x 18 =	\$ 234.		
	P CLAIMS	2 -3=	0	x 84 =	\$ 0.		
				SUB TOTAL	\$ 984.		
				ASSIGNMENT	\$40.		
				TOTAL	\$ 1.024.		
X	Please charge my Deposit Account No. 19-0033 in the amount of \$ 1,024. A duplicate copy of this sheet is enclosed.						
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	X Any additional filing fees required under 37 CFR §1.16.						
	X Any patent application processing fees under 37 CFR §1.17.						
Respectfully submitted, STEPHEN B. ACKERMAN, REG. NO. 37,761							
EXPRESS MAIL CERTIFICATE							
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I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.

Signature / Date